

What is claimed is:

1. A method of boosting the voltage supplied to an output pad driver through a bus connected to a voltage regulator, comprising:
  - momentarily connecting the bus directly to a voltage source;
  - temporarily enabling the voltage regulator to source additional current to an output terminal thereof; and
  - generating a one-shot control pulse, and wherein said one-shot control pulse initiates the momentarily connecting step.
2. A method of boosting the voltage supplied to an output pad driver through a bus connected to a voltage regulator, comprising:
  - momentarily connecting the bus directly to a voltage source;
  - temporarily enabling the voltage regulator to source additional current to an output terminal thereof; and
  - generating a temporary control pulse, and wherein said temporary control pulse initiates the temporarily enabling step.
3. A method of boosting the gate voltages for transistors controlling the voltage appearing on output pads of a solid state memory device, said gate voltages supplied by a voltage regulator through an output bus, comprising:
  - periodically determining the demand for gate voltage and, when the demand is high, momentarily connecting each line of the bus to a voltage source; and
  - temporarily enabling the voltage regulator to source additional current to an output terminal thereof, wherein said temporarily enabling comprises at least one of:
    - temporarily increasing the gate voltage applied to an output transistor of the regulator; and
    - temporarily supplying a boost current to an output terminal of the regulator through a boost transistor.
4. The method of claim 3 additionally comprising generating a one-shot control pulse, and wherein said one-shot control pulse initiates the momentarily connecting step.
5. The method of claim 3 additionally comprising generating a temporary control pulse, and wherein said temporary control pulse initiates the temporarily enabling step.